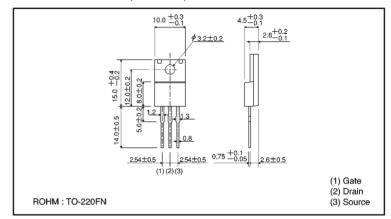
# Switching (450V, 7A) 25K2299N

### Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Wide SOA (safe operating area).
- 4) Gate-source voltage ( $V_{GSS}$ ) guaranteed to be  $\pm 30V$ .
- 5) Easily designed drive circuits.
- 6) Easy to parallel.

# ●Structure Silicon N-channel MOSFET

### External dimensions (Units: mm)



## ●Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		Voss	450	٧
Gate-source voltage		Vgss	±30	٧
Drain current	Continuous	ΙD	7	Α
	Pulsed	IDP*	28	А
Reverse drain current	Continuous	IDR	7	А
	Pulsed	IDRP*	28	А
Total power dissipation(Tc=25°C)		Po	30	W
Channel temperature		Tch	150	င
Storage temperature		Tstg	<b>−55~</b> +150	Ĉ

<sup>\*</sup> Pw $\leq$ 10  $\mu$ s, Duty cycle $\leq$ 1%

# Packaging specifications

	Package	Bulk
Туре	Code	_
	Basic ordering unit (pieces)	500
2SK2299N		0



Transistors 2SK2299N

## ●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Gate-source leakage	lass	_	_	±100	nA	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	V(BR)DSS	450	_	_	٧	In=1mA, VGS=0V
Zero gate voltage drain current	Ibss	_	_	100	μΑ	V <sub>DS</sub> =450V, V <sub>GS</sub> =0V
Gate threshold voltage	VGS(th)	2.0	_	4.0	٧	VDS=10V, ID=1mA
Static drain-source on-state resistance	RDS(on)	_	0.85	1.1	Ω	ID=4.0A, VGS=10V
Forward transfer admittance	Yfs  *	3.0	5.5	_	S	ID=4.0A, VDS=10V
Input capacitance	Ciss	_	870	_	рF	V <sub>DS</sub> =10V
Output capacitance	Coss	_	180	_	рF	V <sub>GS</sub> =0V
Reverse transfer capacitance	Crss	_	40	_	рF	f=1MHz
Turn-on delay time	td(on)	_	15	_	ns	ID=4A, VDD≒150V
Rise time	tr	_	18	_	ns	V <sub>GS</sub> =10V
Turn-off delay time	td(off)	_	60	_	ns	RL=37.5 Ω
Fall time	tf	_	35	_	ns	$R_G=10\Omega$
Reverse recovery time	trr	_	400	_	ns	IDR=7A, VGS=0V
Reverse recovery charge	Qrr		2.5	_	μC	di/dt=100A/ μs

<sup>\*</sup> Pw≦300 μs, Duty cycle≦1%

### Electrical characteristic curves

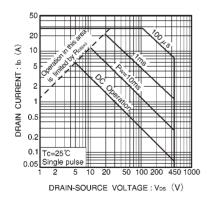


Fig.1 Maximum safe operating area

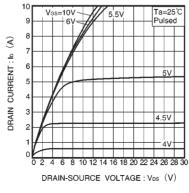


Fig.2 Typical output characteristics

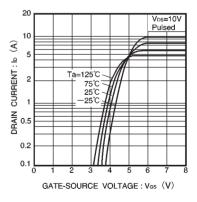
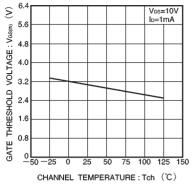


Fig.3 Typical transfer characteristics

**Transistors** 2SK2299N

Pulsed



B<sub>DS(on)</sub> 20 Ta=125℃ STATIC DRAIN-SOURCE ON-STATE RESISTANCE ±75℃ 25°C -25°C 0.5 0.2 0.05 0.1 DRAIN CURRENT: Io (A)

a

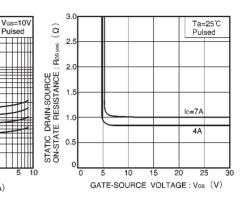
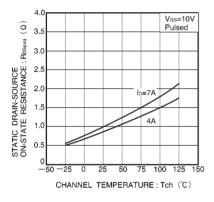


Fig.4 Gate threshold voltage vs. channel temperature

Static drain-source on-state Fig.5 resistance vs. drain current

Fig.6 Static drain-source on-state resistance vs. gate-source voltage



ADMITTANCE : IYISI Vos=10V Pulsed 0.5 TRANSFER 0.2 0.1 25℃ 75℃ 0.02 0.01 0.01 0.02 0.05 0.1 0.2 DRAIN CURI 0.05 0.5 DRAIN CURRENT: ID (A)

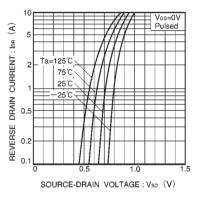


Fig.7 Static drain-source on-state resistance vs. channel temperature

Forward transfer admittance vs. drain current

Reverse drain current vs. source-drain voltage (I)

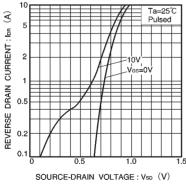
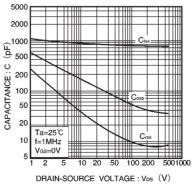
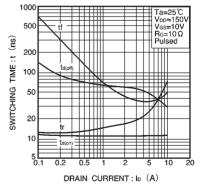


Fig.10 Reverse drain current vs. source-drain voltage (II)



Typical capacitance vs. drain-source voltage



Switching characteristics (See Figures 16 and 17 for the measurement circuit and resultant waveforms)

Transistors 2SK2299N

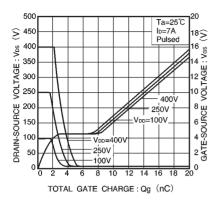


Fig.13 Dynamic input characteristics (See Figure 18 for measurement circuit)

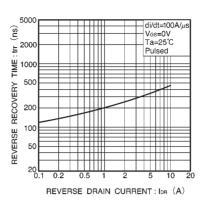


Fig.14 Reverse recovery time vs. reverse drain current

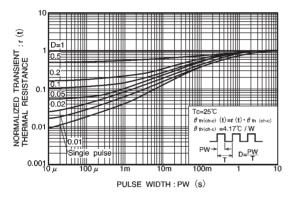
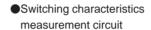


Fig.15 Normalized transient thermal resistance vs. pulse width



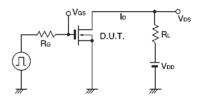


Fig.16 Switching time measurement circuit

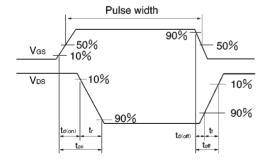


Fig.17 Switching time waveforms

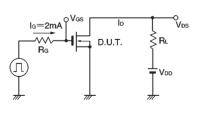


Fig.18 Gate charge measurement circuit



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